

### **REMARKS**

This is in response to the Office Action mailed on September 16, 2004.

Claims 1, 8, 9, 11, 12, 17, 18, 22, 27, 28, 30, 34, 38, 41, 42, 44-46, 48, 50-54, 56, 58, and 59 are amended. Claims 1-61 remain pending in this application.

### **Reservation of the Right to Swear Behind References**

Applicant maintains the right to swear behind any references which are cited in a rejection under 35 U.S.C. §§102(a), 102(e), 103/102(a), and 103/102(e). Statements distinguishing the claimed subject matter over the cited references are not to be interpreted as admissions that the references are prior art.

### **§102 Rejection of the Claims**

**Claims 1-7, 12-16, 22-26, 34, 35, 46, 47, 54, and 55 were rejected under 35 USC § 102(b) as being anticipated by Migliavacca (U.S. Patent No. 6,107,866).**

Independent claim 1 is amended. As amended, claim 1 recites, among other things, “wherein the bandgap reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Applicant is unable to find in Migliavacca “wherein the bandgap reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Accordingly, Applicant requests that the rejection of claim 1 be reconsidered and withdrawn and that claim 1 and its dependent claims 2-7 be allowed.

Independent claim 12 is amended. As amended, claim 12 recites, among other things, an output unit connected to the current generating unit for receiving a version of the generated current for generating “a first bandgap reference voltage and a second bandgap reference voltage different from the first bandgap voltage”. Applicant is unable to find in Migliavacca an output unit connected to the current generating unit for receiving a version of the generated current for generating “a first bandgap reference voltage and a second bandgap reference voltage different from the first bandgap voltage”. Accordingly, Applicant requests that the rejection of claim 12 be reconsidered and withdrawn and that claim 12 and its dependent claims 13-16 be allowed.

Independent claim 22 is amended. As amended, claim 22 recites, among other things, “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Applicant is unable to find in *Migliavacca* “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Accordingly, Applicant requests that the rejection of claim 22 be reconsidered and withdrawn and that claim 22 and its dependent claims 23-26 be allowed.

Independent claim 34 is amended. As amended, claim 34 recites, among other things, “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Applicant is unable to find in *Migliavacca* “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Accordingly, Applicant requests that the rejection of claim 34 be reconsidered and withdrawn and that claim 34 and its dependent claim 35 be allowed.

Independent claim 46 is amended. As amended, claim 46 recites, among other things, “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Applicant is unable to find in *Migliavacca* “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Accordingly, Applicant requests that the rejection of claim 46 be reconsidered and withdrawn and that claim 46 and its dependent claim 47 be allowed.

Independent claim 54 is amended. As amended, claim 54 recites, among other things, “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Applicant is unable to find in *Migliavacca* “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Accordingly, Applicant requests that the rejection of claim 54 be reconsidered and withdrawn and that claim 54 and its dependent claim 55 be allowed.

**Claims 1, 10, 12, 19-21, 22, 29, 36-38, 40, 42, 44, 46, 53, 54, and 58 were rejected under 35 USC § 102(e) as being anticipated by Eshraghi et al. (U.S. Patent No. 6,737,849).**

Applicant respectfully traverses.

Independent claim 1 recites, among other things, a first control transistor connected between the first internal node and a second supply node, the first control transistor and a portion of the current mirror forming a path between the first and second supply nodes, “wherein the path includes only two transistors”. Applicant is unable to find in Eshraghi et al. a first control transistor connected between the first internal node and a second supply node, the first control transistor and a portion of the current mirror forming a path between the first and second supply nodes, “wherein the path includes only two transistors”. Accordingly, Applicant requests that the rejection of claim 1 be reconsidered and withdrawn and that claim 1 and its dependent claim 10 be allowed.

Independent claim 12 recites, among other things, a current generating unit connected to the first and second supply nodes for providing a generated current, the current generating unit including a current path connected between the first and second supply nodes, “wherein the current path includes only two transistors”. Applicant is unable to find in Eshraghi et al. a current generating unit connected to the first and second supply nodes for providing a generated current, the current generating unit including a current path connected between the first and second supply nodes, “wherein the current path includes only two transistors”. Accordingly, Applicant requests that the rejection of claim 12 be reconsidered and withdrawn and that claim 12 and its dependent claims 19-21 be allowed.

Independent claim 22 is amended. As amended, claim 22 recites, among other things, “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Applicant is unable to find in Eshraghi et al. “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Accordingly, Applicant requests that the rejection of claim 22 be reconsidered and withdrawn and that claim 22 and its dependent claim 29 be allowed.

Dependent claims 36 and 37 depend from independent claim 34. Thus, claims 36 and 37 also recite the things recited in claim 34 such as “a current generating unit connected to the first and second supply nodes for providing a generated current, the current generating unit including a current path connected between the first and second supply nodes” and “wherein the current

path includes only two transistors”. Applicant is unable to find in Eshraghi et al. as “a current generating unit connected to the first and second supply nodes for providing a generated current, the current generating unit including a current path connected between the first and second supply nodes” and “wherein the current path includes only two transistors”. Accordingly, Applicant requests that the rejection of claims 36 and 37 be reconsidered and withdrawn and that claims 36 and 37 be allowed.

Independent claim 38 recites, among other things, a current generating unit connected to the first and second supply nodes for providing a generated current, the current generating unit including a current path connected between the first and second supply nodes, “wherein the current path includes only two transistors”. Applicant is unable to find in Eshraghi et al. a current generating unit connected to the first and second supply nodes for providing a generated current, the current generating unit including a current path connected between the first and second supply nodes, “wherein the current path includes only two transistors”. Accordingly, Applicant requests that the rejection of claim 38 be reconsidered and withdrawn and that claim 38 and its dependent claim 41 be allowed.

Independent claim 42 recites, among other things, a current generating unit connected to the first and second supply nodes for providing a generated current, the current generating unit including a current path connected between the first and second supply nodes, “wherein the current path includes only two transistors”. Applicant is unable to find in Eshraghi et al. a current generating unit connected to the first and second supply nodes for providing a generated current, the current generating unit including a current path connected between the first and second supply nodes, “wherein the current path includes only two transistors”. Accordingly, Applicant requests that the rejection of claim 42 be reconsidered and withdrawn and that claim 42 and its dependent claim 44 be allowed.

Independent claim 46 recites, among other things, “generating a generated current in a current path of a current generating unit having elements with positive temperature coefficient and elements with negative temperature coefficient, *the current path having only two transistors* connected in series between a first supply node and a second supply node”. Applicant is unable to find in Eshraghi et al. “generating a generated current in a current path of a current generating unit having elements with positive temperature coefficient and elements with negative

temperature coefficient, *the current path having only two transistors* connected in series between a first supply node and a second supply node”. Accordingly, Applicant requests that the rejection of claim 46 be reconsidered and withdrawn and that claim 46 and its dependent claim 53 be allowed.

Independent claim 54 recites, among other things, sourcing a first current using “a first transistor connected directly” to a supply node, passing the first current directly through a “first control transistor connected directly” to a second supply node, sourcing a second current using “a second transistor connected directly” to the first supply node, and passing the second current “directly through a combination of a second control transistor and a resistive element connected to the second supply node”. Applicant is unable to find in Eshraghi et al. sourcing a first current using “a first transistor connected directly” to a supply node, passing the first current directly through a “first control transistor connected directly” to a second supply node, sourcing a second current using “a second transistor connected directly” to the first supply node, and passing the second current “directly through a combination of a second control transistor and a resistive element connected to the second supply node”. Accordingly, Applicant requests that the rejection of claim 54 be reconsidered and withdrawn and that claim 54 and its dependent claim 58 be allowed.

### **§103 Rejection of the Claims**

Claims 38, 39, 42, and 43 were rejected under 35 USC § 103(a) as being unpatentable over Migliavacca.

Independent claim 38 is amended. As amended, claim 38 recites, among other things, “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Applicant is unable to find in Migliavacca “wherein the reference voltage includes a stable voltage level when one of the first and second supply nodes includes a voltage of about 1.3 volts to about 1.5 volts”. Accordingly, Applicant requests that the rejection of claim 38 be reconsidered and withdrawn and that claim 38 and its dependent claim 39 be allowed.

Independent claim 42 is amended. As amended, claim 42 recites, among other things, an output unit connected to the current generating unit for receiving a version of the generated

current for generating “multiple bandgap reference voltages”. Applicant is unable to find in Migliavacca an output unit connected to the current generating unit for receiving a version of the generated current for generating “multiple bandgap reference voltages”. Accordingly, Applicant requests that the rejection of claim 42 be reconsidered and withdrawn and that claim 42 and its dependent claim 43 be allowed.

**Allowable Subject Matter**

Claims 8, 9, 11, 17, 18, 27, 28, 30-33, 41, 45, 48-52, 56, 57, and 59-61 were objected to as being dependent upon a rejected base claim, but were indicated to be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

These dependent claims 8, 9, 11, 17, 18, 27, 28, 30-33, 41, 45, 48-52, 56, 57, and 59-61 are selectively rewritten only to put these dependent claims in independent form. The scope of the rewritten claims is not narrowed. Thus, claims 8, 9, 11, 17, 18, 27, 28, 30-33, 41, 45, 48-52, 56, 57, and 59-61 are now in condition for allowance.

**CONCLUSION**

Applicant respectfully submits that the claims are in condition for allowance, and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's representative at (612) 373-6969 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.


Respectfully submitted,

JEFFREY KOELLING

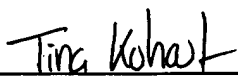
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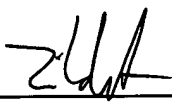
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**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 16 day of December, 2004.

  
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